

REMARKS

Applicant has amended the claims to address the various technical objections of the Examiner referred to in paragraph 2 and the 35 U.S.C. 112 rejections referred to in paragraphs 3-7 of the Official Action. Claim 1 has been amended to combine all of its limitations to those of claim 2 and claim 2 has been cancelled. This is equivalent to an incorporation of the limitations of claim 1 into claim 2 and a cancellation of claim 1. Claims 9 and 11 have also been cancelled.

The claim objections as to claims 14 and 15 have all been addressed.

The claim rejections under 35 U.S.C. 112 have also all been addressed.

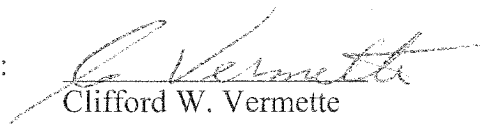
The aligner block can de-skew bit arrival times at the receiving end of each interface on the data and control lines by allowing the receiving end to correct for relative skew differences of up to +/- 1 bit time. adjusting the bit selection time relative skew between lines.

It is believed that all of the points of objection and rejection have been addressed and the application is now in condition for allowance. Re-consideration of the application herein is respectfully solicited.

Respectfully submitted

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by:



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